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APPLICANTS

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** CONTINUING DATA ***** *EPM*** FOREIGN APPLICATIONS ***** *EPM*

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Verifed and Acknowledged <i>EPM</i> Examiner's Signature <i>EPM</i> Initials	STATE OR COUNTRY CA	SHEETS DRAWING 13	TOTAL CLAIMS 22	INDEPENDENT CLAIMS 9
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TITLE

Multi-cell resistive memory array architecture with select transistor

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